Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-18 (Canceled).

19. (New) An arrangement, comprising:

a processor including a plurality of cells configurable with regard to at least one of function and interconnections;

at least one memory,

an address arrangement configured to address the at least one memory; and

an internal global bus system communicatively coupled to the address arrangement, the plurality of cells being connectable via the internal global bus system;

wherein the address arrangement is further configured to indicate an end of a data transfer via a signal on a bus.

20. (New) The arrangement according to claim 19, wherein the address arrangement is an address generator.